

10/073450



PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10073450	FILING DATE 02/11/2002	CLASS 385 359-	SUBCLASS 11	GAU 2633	EXAMINER Rojas
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**APPLICANTS: McGlashan-Powell Maurice; Hobbs Philip;

**CONTINUING DATA VERIFIED: *on*

** FOREIGN APPLICATIONS VERIFIED: *on*

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials *on*

ATTORNEY DOCKET NO

YOR920020009US1

TITLE : Magneto-optical switching backplane for processor interconnection

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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